

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	953946	"324"/\$.ccls. and logic adj test or circuit and gate and inverter or buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 12:40
L2	5324	"324"/\$.ccls. and ((logic adj test) or circuit and gate and (inverter or buffer))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 12:41
L3	671	2 and exclusive	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 12:42
L4	35	3 and (combinatorial or combinational)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 12:44
L5	70	3 and bypass	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 12:45
L6	178	3 and multiplexer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 12:45
L7	178	3 and multiplexer\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 13:29
L8	88	3 and multiplexer\$1 and pattern\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 13:29